

**REMARKS**

Entry of this paper in reply to Paper No. 29 issued on the 27<sup>th</sup> of October 2004 is respectfully requested.

**Requirement for Information Under 37 C.F.R. §1.105**

Pursuant to 37 C.F.R. §1.105, the Examiner requested that the Applicants:

“provide the citations and a copy of each publication that any of the applicants relied upon to develop the disclosed subject matter that describes the applicant’s invention, particularly at [*sic*] to the ‘conventional system power management apparatus of figure 1’ including but not limited to the conventional DPMS VESA standards.”

The Examiner’s requirement has not specified whether the Examiner is referring to Applicants’ parent application Serial No. 08/814,502 issued as U.S. Patent No. 5,944,803 on the 31<sup>st</sup> of August 1999, or whether the requirement is specific to Applicants’ instant reissue patent application Serial No. 09/942,961 filed on the 31<sup>st</sup> of August 2001. If the requirement is specific to one application, as opposed to both applications, written clarification is respectfully requested in subsequent Office correspondence.

To the Extent that the Examiner’s requirement is general, and not specific to a particular application, Applicants list and provide copies of the following twelve (12) English language references, with the caveat that pursuant to 37 C.F.R. §1.105(a)(vii)(3), the information required to be submitted is unknown and/or is not readily available to the party or parties from which it was requested. Moreover, one of the co-inventors, Mr. Seung-Cheol Hong is no longer

employed by the Assignee and his whereabouts are unknown to the Assignee, and the second co-inventor, Mr. Ki-Young Jang, has been on an extended leave from his Office, and the Assignee has been unable to contact Mr. Jang.

1. U.S. Patent No. 5,389,952 to Kikinis, entitled *LOW-POWER-CONSUMPTION MONITOR STANDBY SYSTEM*, issued on February 14, 1995.
2. U.S. Patent No. 5,736,873 to Hwang, entitled *POWER SAVING CONTROL CIRCUIT FOR A DISPLAY APPARATUS*, issued on April 7, 1998.
3. Printout from Wikipedia, the free encyclopedia, entitled "*VESA Display Power Management Signaling*", 1 page.
4. Printout from VESA: Video Electronics Standards Association, *VESA-Standards: What Are They?*, pp 1-9.
5. Printout from The Free Dictionary com by Farlex, entitled *VESA Display Power Management Signaling*", pp. 1-2.
6. *VESA Advanced Feature Connector (VAFC) Software Interface Standard*, by Video Electronics Standard Association, Milpitas, CA 95035, Version 1.0, Revision date March 30, 1994, 1 page.
7. *VESA Advanced Feature Connector (VAFC) Software Interface Standard*, by Video Electronics Standard Association, Milpitas, CA 95035, Version 1.1, Revision date November 30, 1995, 1 page.
8. Printout from VESA: Video Electronics Standards Association, *VESA-*

*Standards: Summaries*, pp 1-4.

9. *MI Display Interface System*, by Video Electronics Standard Association, Milpitas, CA 95035, Version 1.0, August 16, 2001 1 page.
10. *VESA BIOS Extensions/Power Management, (VBE/PM), Standard*, by Video Electronics Standard Association, Milpitas, CA 95035, Version 1.0, Approved: February 4, 1994, 1 page.
11. Printout from [www.lextron.com](http://www.lextron.com), *HP AND ELONEX ANNOUNCE PATENT LICENSING AGREEMENT*, Elonex, Hewlett Packard, 18 November 2004, 2 pages.
12. *The Evolution of the Green PC: Towards Integrated Power Management*, by Gary Smerdon, Advanced Micro Devices, Network Products Division, Sunnyvale, CA, 1 page.

An Information Disclosure Statement pursuant to 37 C.F.R. §1.97 listing these twelve (12) references accompanies this response. Applicants note that “the conventional DPMS VESA standards”<sup>1</sup> are provided, to the extent of those standards are available. The August 1993 *Display Power Management Signaling (DPMS) Standard 1.0*, which is believed to be Revision 1, could not be obtained. Applicants have instead, provided the *VESA Display Power Management Signaling* from Wikipedia showing the relation between power use, the horizontal

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<sup>1</sup> Paper No. 29 (10/27/04), Examiner’s Comments, page 2.

and vertical sync signals, and status of those synchronization signals, together with the same information provided in the VESA Display Power Management Signaling except provided by *The Free Dictionary Com*, the paper entitled *The Evolution of the Green PC: Towards Integrated Power Management* provides a background discussion of *VE Display Power Management Signaling Standard 1.0 (DPMS)*; this paper is undated, and appears to have been written during 1997.

Applicants further note that references such as the Kikinis '952 U.S. patent is of record in the parent application, and by continuation, in the instant reissue application. The foregoing references are supplied in compliance with the Examiner's request for "the conventional DPMS VESA standards"<sup>2</sup>; there is no indication, in accordance with 37 C.F.R. §1.105(a)(vii)(3) of which information "the applicants relied upon to develop the disclosed subject matter that describes the applicants' invention." Accordingly, Applicants have complied with the Examiner's requirement under 37 C.F.R. §1.105; should the Examiner desire additional art, the Examiner is respectfully requested to telephone Applicants' undersigned attorney.

#### **Concise Explanation of Relevance**

The second prong of the Examiner's requirement under 37 C.F.R. §1.105 requested Applicants to "provide a concise explanation of the reliance placed on that publication in the development of the disclosed subject matter."<sup>3</sup> Considering each of the twelve listed references

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<sup>2</sup> Paper No. 29 (10/27/04), Examiner's Comments, page 2.

<sup>3</sup> Paper No. 29 (10/27/04), Examiner's Comments, page 2.

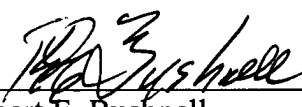
provided in accordance with the Examiner's earlier request, the apparatus defined by Applicant's pending claims is advantageously enabled to substantially reduce power consumed by the monitor by switching off application of electrical power to the heater to the monitor without concurrently requiring an auxiliary power supply unit to furnish electrical energy to the regulator as well as the microcomputer as is required by the circuit shown in Figure 1, and as would be required by circuits designed to implement the concept disclosed in the twelve (12) references furnished in compliance with the Examiner's request. It may be noted that in the Kinkinis, U.S. Patent No. 5,389,952 reference, switch 553 may not be said to be "disposed between" power supply 555 and a heater of a monitor. Accordingly, dependent claims are deemed to be patentably distinguishable and allowable over the prior art.

#### **Fee and Certification Requirements**

Per paragraph 4 on page 2 of Paper No. 29, the fee and certification requirements under 37 C.F.R. §1.97 were waived for the accompanying Information Disclosure Statement and PTO-1449. Accordingly, no fee is incurred by this Response.

In view of the above, entry of the foregoing Response in reply to Paper No. 29 is respectfully requested. Should any questions remain unresolved, the Examiner is requested to telephone Applicants' attorney.

Respectfully submitted,

  
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